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ISSUE CLASSIFICATION	
Class	Subclass
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PATENT NUMBER

U.S. UTILITY Patent Application

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SCANNED	TRJ. Q.A.	U1

APPLICATION NO. 09/886225	CONT/PRIOR F	CLASS 707 714	SUBCLASS 718	ART UNIT -3661 2433	EXAMINER J. H. [Signature] [Signature]
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APPLICANTS

Chen-Tsai Lee

TITLE

Memory interlace-checking method

PTO-2040
12/89[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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